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(Not for submission under 37 CFR 1.99)

Application Number	10674606
Filing Date	2003-09-30
First Named Inventor	Levine et al.
Art Unit	2183
Examiner Name	Fiegle, Ryan Paul
Attorney Docket Number	AUS920030485US1

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Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
RF ↓	1	5657253		1997-08-12	Dreyer et al.	
	2	5991708		1999-11-23	Levine et al.	
	3	5564015		1996-10-08	Bunnell	
	4	5450349		1995-09-12	Brown et al.	
	5	5987598		1999-11-16	Levine et al.	
	6	6009514		1999-12-18	Henzinger et al.	
	7	6112317		2000-08-29	Berc et al.	
	8	5896538		1999-04-20	Blandy et al.	

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RF	9	5355487		1994-10-11	Keller et al.	
RF	10	6948032	B2	2005-09-20	Kadambi et al.	

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RF	1	"CPU Cache", Wikipedia, pp. 1-14 retrieved 11/1/06 http://en.wikipedia.org/wiki/CPU_cache	<input type="checkbox"/>
RF	2	AMMONS et al., "Exploiting Hardware Performance Counters with Flow and Context Sensitive Profiling", 1997, ACM, pp. 85-96.	<input type="checkbox"/>

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RF	1	5966538		1999-10-12	Granston et al.	
	2	5940618		1999-08-17	Blandy et al.	
	3	6189142	B1	2001-02-13	Johnston et al.	
	4	5768500		1998-06-16	Agrawal et al.	
	5	6026235		2000-02-15	Shaughnessy	
	6	5950003		1999-09-07	Kaneshiro et al.	
	7	6594820	B1	2003-07-15	Ungar	
	8	6973417	B1	2005-12-06	Maxwell et al.	

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RF	9	6721875	B1	2004-04-13	McCormick et al.	
↓	10	6944720	B2	2005-09-13	Sperber et al.	
	11	7035996	B2	2006-04-25	Woodall et al.	

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RF	1	20020073406	A1	2002-06-13	Gove	
↓	2	20030066055	A1	2003-04-03	Spivey	
	3	20030131343	A1	2002-07-10	French et al.	
	4	20040268316	A1	2004-12-30	Fisher et al.	

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RF	1	5761103		1998-06-02	Oakland et al.	
RF	2	6549959	B1	2003-04-13	Yates et al.	
RF	3	6772322	B1	2004-08-03	Merchant et al.	
RF	4	5937437		1999-08-10	Roth et al.	
RF	5	6070009		2000-05-30	Dean et al.	
RF	6	6381679	B1	2002-04-30	Matsubara et al.	
RF	7	6687794	B2	2004-02-03	Malik	
RF	8	6539458	B2	2003-03-25	Holmberg	

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RF	9	6973542	B1	2005-12-06	Schmuck et al.	
RF	10	6782454	B1	2004-08-24	Damron	
RF	11	5652858		1997-06-29	Okada et al.	
RF	12	6848029	B2	2005-01-25	Coldewey	
RF	13	5822790		1998-10-13	Mehrotra	

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RF	1	"Method for the dynamic prediction of nonsequential memory accesses", September 25, 2002, pp. 1-4, ip.com IPCOM000009888D	<input type="checkbox"/>
RF	2	"Cache Miss Director - A Means of Prefetching Cache Missed Lines", August 1, 1982, IBM Technical Disclosure Bulletin, Vol. 25, Issue 3A, pp.1286	<input type="checkbox"/>

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Application Number **10/674,606**
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 Examiner Name **Fiegle, Ryan Paul**
 Art Unit **2183**
 Attorney Docket No. **AUS920030485US1**

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
RF	AA 6,149,318	11-21-2000	Chase et al.	
↓	AB 6,185,671 B1	02-06-2001	Pentovski et al.	
↓	AC 6,247,113 B1	06-12-2001	Jaggar	
↓	AD 6,460,135 B1	10-01-2002	Suganuma	
↓	AE 2003/0126590 A1	07-03-2003	Burrows et al.	
↓	AF 6,842,850 B2	01-11-2005	Ganapathy et al.	

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RF AG	MERTEN et al., "A Hardware Driven Profiling Scheme for Identifying Program Hot Spots to Support Runtime Optimization", IEEE, 1999, pp. 136-147.

Examiner: /Ryan Fiegle/

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RF	AA	5,857,097	01-05-1999	Henzinger et al.
↓	AB	6,820,155	11-16-2004	Ito
	AC	5,745,770	04-28-1998	Thangadural et al.

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